**General Description**

The LM1596/LM1496 are doubled balanced modulator-demodulators which produce an output voltage proportional to the product of an input (signal) voltage and a switching (carrier) signal. Typical applications include suppressed carrier modulation, amplitude modulation, synchronous detection, FM or PM detection, broadband frequency doubling and chopping.

The LM1596 is specified for operation over the −55°C to +125°C military temperature range. The LM1496 is specified for operation over the 0°C to +70°C temperature range.

**Features**

- Excellent carrier suppression
  - 65 dB typical at 0.5 MHz
  - 50 dB typical at 10 MHz
- Adjustable gain and signal handling
- Fully balanced inputs and outputs
- Low offset and drift
- Wide frequency response up to 100 MHz

**Schematic and Connection Diagrams**

The diagrams show the pin configurations and connections for the LM1596/LM1496 balanced modulator-demodulators.

**Order Number**

- LM1496H or LM1596H
- LM1496M or LM1496N

**Notes**

- Pin 10 is connected electrically to the case through the device substrate.
- See NS Package Number H08C or M14A or N14A.
## Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

- Internal Power Dissipation (Note 1) 500 mW
- Applied Voltage (Note 2) 30V
- Differential Input Signal (V7 – V8) ±5.0V
- Differential Input Signal (V4 – V1) ±(5 + I5R0)V
- Input Signal (V2 – V1, V3 – V4) 5.0V
- Bias Current (I5) 12 mA

### Operating Temperature Range

- LM1596: –55°C to +125°C
- LM1496: 0°C to +70°C

### Storage Temperature Range

- LM1596: –65°C to +150°C
- LM1496: –65°C to +125°C

## Soldering Information

- Dual-In-Line Package
  - Soldering (10 seconds) 260°C
- Small Outline Package
  - Vapor Phase (60 seconds) 215°C
  - Infrared (15 seconds) 220°C

See AN-450 “Surface Mounting Methods and their effects on Product Reliability” for other methods of soldering surface mount devices.

## Electrical Characteristics (TA = 25°C, unless otherwise specified, see test circuit)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM1596</th>
<th>LM1496</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Carrier Feedthrough</td>
<td>Vc – 60 mVrms sine wave</td>
<td>40</td>
<td>40</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Ic – 1.0 kHz, offset adjusted</td>
<td>140</td>
<td>140</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Vc – 80 mVrms sine wave</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Ic – 1.0 kHz, offset adjusted</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Vc – 300 mVrms square wave</td>
<td>0.04</td>
<td>0.2</td>
<td>0.2</td>
</tr>
<tr>
<td></td>
<td>Ic – 1.0 kHz, offset adjusted</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Vc – 300 mVrms square wave</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Ic – 1.0 kHz, not offset adjusted</td>
<td>20</td>
<td>100</td>
<td>20</td>
</tr>
<tr>
<td>Carrier Suppression</td>
<td>I5 – 10 kHz, 300 mVrms</td>
<td>65</td>
<td>50</td>
<td>65</td>
</tr>
<tr>
<td></td>
<td>I5 – 500 kHz, 60 mVrms sine wave offset adjusted</td>
<td>50</td>
<td>50</td>
<td></td>
</tr>
</tbody>
</table>
| Transadmittance Bandwidth             | R5 = 500Ω  
Carrier Input Port, Vc – 60 mVrms sine wave  
I5 – 1.0 kHz, 300 mVrms sine wave  
Signal Input Port, V5 = 300 mVrms sine wave  
V7 – V8 = 0.5 Vdc                        | 300 | 300 |     |     |     |     | MHz          |
| Voltage Gain, Signal Channel          | V5 = 100 mVrms, f = 1.0 kHz  
V7 – V8 = 0.5 Vdc                        | 2.5 | 3.5 | 2.5 | 3.5 |     |     | V/V          |
| Input Resistance, Signal Port         | f = 5.0 MHz  
V7 – V8 = 0.5 Vdc                        | 200 | 200 |     |     |     |     | kΩ           |
| Input Capacitance, Signal Port        | f = 5.0 MHz  
V7 – V8 = 0.5 Vdc                        | 2.0 | 2.0 |     |     |     |     | pF           |
| Single Ended Output Resistance        | f = 10 MHz                                                   | 40  | 40  |     |     |     |     | kΩ           |
| Input Bias Current                    | (I1 + I4)/2                                               | 5.0 | 5.0 |     |     |     |     | μA           |
| Input Bias Current                    | (I7 + I9)/2                                               | 12  | 25  | 12  | 30  |     |     | μA           |
| Input Offset Current                  | (I1 – I4)                                               | 0.7 | 5.0 | 0.7 | 5.0 |     |     | μA           |
| Input Offset Current                  | (I7 – I9)                                               | 0.7 | 5.0 | 0.7 | 5.0 |     |     | μA           |
| Average Temperature Coefficient of Input Offset Current | (−55°C < TA < +125°C)  
(0°C < TA < +70°C)                        | 2.0 | 2.0 |     |     |     |     | nA/°C        |
| Output Offset Current                 | (I5 – I3)                                               | 14  | 50  | 14  | 60  |     |     | μA           |
| Average Temperature Coefficient of Output Offset Current | (−55°C < TA < +125°C)  
(0°C < TA < +70°C)                        | 90  | 90  |     |     |     |     | nA/°C        |
### Electrical Characteristics (T_A = 25°C, unless otherwise specified, see test circuit) (Continued)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM1596</th>
<th>LM1496</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Signal Port Common Mode</td>
<td>Input Voltage Range f_s = 1.0 kHz</td>
<td>5.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Signal Port Common Mode</td>
<td>Rejection Ratio V_T - V_B = 0.5 Vdc</td>
<td>-85</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Common Mode Quiescent</td>
<td>Output Voltage</td>
<td>8.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Differential Output Swing</td>
<td>Capability</td>
<td>8.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Positive Supply Current (I_g + I_p)</td>
<td></td>
<td>2.0</td>
<td>3.0</td>
<td>2.0</td>
</tr>
<tr>
<td>Negative Supply Current (I_10)</td>
<td></td>
<td>3.0</td>
<td>4.0</td>
<td>3.0</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td></td>
<td>33</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Note 1:** LM1596 rating applies to case temperatures to +125°C; derate linearly at 6.5 mW/°C for ambient temperature above 75°C. LM1496 rating applies to case temperatures to +70°C.

**Note 2:** Voltage applied between pins 6-7, 8-1, 9-7, 9-8, 7-1, 6-8, 2-5, 3-5.

**Note 3:** Refer to revs1596x drawing for specifications of military LM1596H versions.

### Typical Performance Characteristics

- **Carrier Suppression vs Input Level**
- **Carrier Suppression vs Frequency**
- **Carrier Feedthrough vs Frequency**
- **Sideband Output vs Carrier Levels**
- **Sideband and Signal Port Transadmittances vs Frequency**
- **Signal-Port Frequency Response**
This figure shows the LM1596 used as a single sideband (SSB) suppressed carrier demodulator (product detector). The carrier signal is applied to the carrier input port with sufficient amplitude for switching operation. A carrier input level of 300 mVrms is optimum. The composite SSB signal is applied to the signal input port with an amplitude of 5.0 to 500 mVrms. All output signal components except the desired demodulated audio are filtered out, so that an offset adjustment is not required. This circuit may also be used as an AM detector by applying composite and carrier signals in the same manner as described for product detector operation.
The frequency doubler circuit shown will double low-level signals with low distortion. The value of C should be chosen for low reactance at the operating frequency. Signal level at the carrier input must be less than 25 mV peak to maintain operation in the linear region of the switching differential amplifier. Levels to 50 mV peak may be used with some distortion of the output waveform. If a larger input signal is available a resistive divider may be used at the carrier input, with full signal applied to the signal input.
Physical Dimensions inches (millimeters)

Metal Can Package (H)
Order Number LM1496H or LM1596H
NS Package Number H08C

Molded Small Outline Package (M)
Order Number LM1496M
NS Package Number M14A
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